

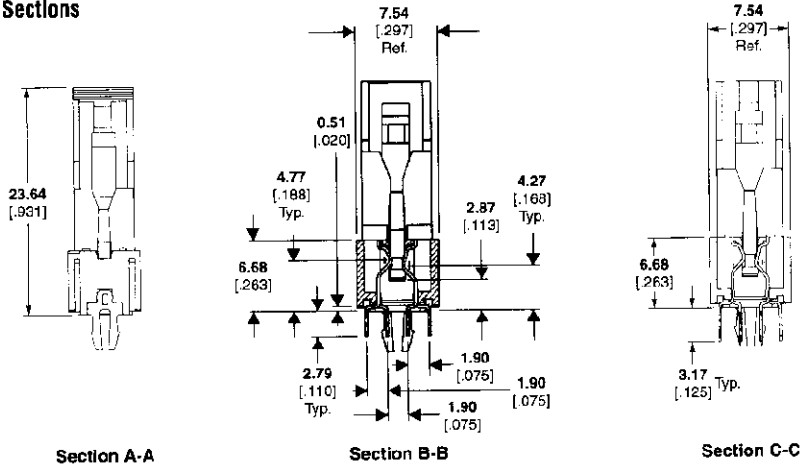
**Vertical Dual Read-Out for 8 Byte Memory Modules** (Continued)

**168 Positions — JEDEC MO-161** (Continued)

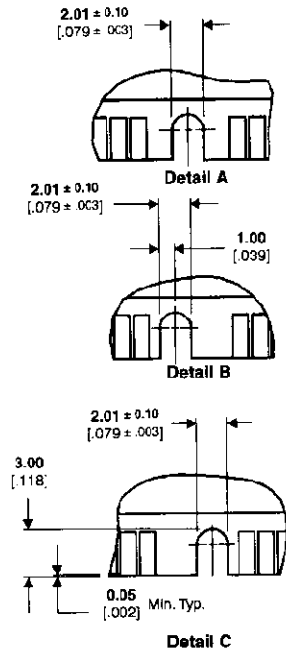
**Material and Finish**

**Housing** — High temperature thermoplastic; color: natural  
**Contact** — Phosphor bronze with gold flash over 0.00051 [0.00020] min. thick palladium-nickel over 0.00127 [0.00050] min. thick nickel in contact area and 0.00381 [0.00150] min. thick tin-lead over 0.00127 [0.00050] min. thick nickel in solder tail area.

**Cross Sections**



**Module Keying Details**  
(See Page 20 for Complete Module Layout)



Part Numbers		
Std. DRAM 3.3 Volt	Sync. DRAM 3.3 Volt	Non-Std. (Non-Buff.) DRAM 3.3 Volt
Key 1 = A Key 2 = A	Key 1 = B Key 2 = A	Key 1 = C Key 2 = A
390074-4	390074-5	390074-6
390215-4	390215-5	390215-6

**Note:** See page 20 for Key 1 and Key 2 positions on module layout. DIMM IIP (Performance Family) product available as replacement for these part numbers. See cross reference at left. Contact AMP Incorporated for additional information.

**Cross Reference — Standard to DIMM IIP Sockets**

Standard	DIMM IIP
390074-4	390168-4
390074-6	390168-1
390215-4	390240-4
390215-6	390240-1